



Case Study

Architecture, IP Design and verification of Automotive IC

Project/Client Details

- SoC for the ADAS market for acquisition and inference of sense data
- Cortex R5F based CPU Subsystem with Tensilica P5 image processing element
- US Startup in the Automotive space

Design Scope

- SoC Architecture
- Functional Safety architecture of an ASIL-D Compliant Automotive Chip
- Security architecture for the IC taking care of exhaustive life cycle management requirements
- Design and verification of 8 channel Analog Digital Interface
- Design of FFT Engine
- eFUSE IP development
- SoC and IP level verification

Solution/Highlights

Execution Highlights

- Nine member team working on multiple front end aspects including architecture of safety and security
- 9 month schedule from architecture to tape-out
- Program management ownership to ensure deliverables assigned to Ignitarium meet schedule, quality guidelines
- Highly complex DSP IP design supporting multiple modes of operation and with features of self test inbuilt

Tools/Technologies

- Cadence tools



THANK YOU



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