Case Study

Verification of IoT Gateway Device ICs

Project/Client Details

- Cortex M3 based IoT Gateway device to convert Radio packets into IP Packets
- Tier 1 Semiconductor Company

Design Scope

- Digital verification of multiple Cortex M3 based ICs over 1.5 years
  - System Verilog / UVM based Verification
  - Test Planning using V-Planner
  - Block level and SoC level verification – Cache controller, Flash, SRAM, DDR, USB, ENET, SPI, I2C
  - Test plan & Test case development in SV and C based
  - IP and SoC level Coverage analysis

Solution/Highlights

Execution Highlights
- Two ICs taped out successfully and functional first pass
- Achieving coverage goals

Tools/Technologies

- SV-UVM, NCSIM, Verification Planner

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Spark On.

THANK YOU

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