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Case Study

Mixed Signal Verification for a Single Cell Lithium Ion Battery Charger

Design Scope

- Highly integrated single cell Li-Ion battery charger and system power path management device targeted for space-limited, portable applications.
- System Verilog Verification Environment.
- Bring in advanced verification concepts like Randomization, Functional Coverage and Assertions into Mixed Signal Verification.

Client

- Tier 1 Semiconductor Company

Tools/Technologies

- SV-UVM, NCSIM, Verification Planner

Solution/Highlights

Execution Highlights

- Functional Coverage Implementation.
- System Verilog Assertion Checks.
- Random Stimulus.
- Automation for faster turn around
- Multi-site execution over remote link



THANK YOU



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