



Case Study

Multimedia SoC Architecture

Project/Client Details

- ARM based Multimedia SoC with multi A9 processor cores, Video processor, Graphics processor and Display processor
- Responsible for defining SoC specification

Design Scope

- SoC Architecture
- Responsible for IP Evaluation and Selection
- Responsible for analysis of Power, Performance, Area
- Bottlenecks analysis using custom templates
- Low Power Architecture
- Security Architecture
- Debug and Trace Architecture
- Program Management

Solution/Highlights

Execution Highlights

- First SoC development by the customer
- Architect team size of 5
- Building multi ARM core SoC from scratch
- Mapping customer requirements to SoC architecture
- Managing multiple vendors such as IP vendors, EDA tool vendors
- Extensive involvement till chip bring up to ensure Quality/performance goals are met

Tools/Technologies

- Tools from multiple EDA vendors



THANK YOU



email: info@ignitarium.com

Ignitarium Technology Solutions Pvt. Ltd.

Bangalore, India
#2615, 3rd Floor,
27th Main Road, Sector 1,
HSR Layout,
Bangalore – 560102
Phone: +91-80-30723694

Kochi, India
5th Floor, Crescens Tower,
NH-47, Changampuzha Nagar,
Kochi - 682033
Phone: +91 484 2933073

Ignitarium Inc.

United States
2570 N. First Street,
Suite 200 San Jose, CA 95131
Phone: +1 512 640 3488