Case Study

Silicon Validation: Multicore ARM Network SoC

Project/Client Details

- ARM based Network SoC with PCIe, NAND, DDR SPI, QSPI and DMA Controller
- Global supplier of a broad range of digital and analog semiconductor connectivity solutions
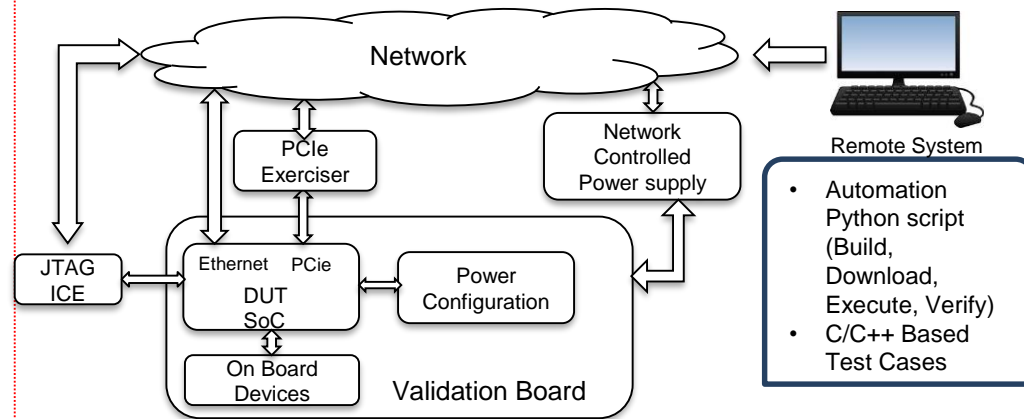
Design Scope

- Post and Pre silicon functional validation of multiple peripherals such as PCIe Gen2, NAND, DDR, SPI, QSPI, DMA Controller and Cache Coherency module
- PVT and power characterization tests
- Test automation

Expertise/Tools

- PCIe exerciser and analyzer, PVT setup with thermal head
- Emulator and Silicon validation board
- Network traffic generator
- C/C++ based test case, Python Automation

Highlights

- Multi- year engagement spanning multiple variants
- Multi master coherent architecture aware validation
- Full ownership from test plan to debugging
- Development of Bare metal test cases for validation
- Bus transaction level debugging in emulation environment

THANK YOU

email: info@ignitarium.com

Ignitarium Technology Solutions Pvt. Ltd.

Ignitarium Inc.

Bangalore, India
#2615, 3rd Floor,
27th Main Road, Sector 1,
HSR Layout,
Bangalore – 560102
Phone: +91-80-30723694

Kochi, India
5th Floor, Crescens Tower,
NH-47, Changampuzha Nagar,
Kochi - 682033
Phone:+91 484 2933073

United States
2570 N. First Street,
Suite 200 San Jose, CA 95131
Phone: +1 512 640 3488

ignitarium
Spark On.