Case Study

Silicon Validation: Audio DSP Algorithm

Project/Client Details

- Firmware development & Automation of the environment for SW validation
- Tier 1 Semiconductor Company

Design Scope

- Validation of DSP algorithm in an Audio IC.
- Pre and Post silicon functional validation
- Test script development, execution and result analysis
- Test automation

Expertise/Tools

- National instruments DAQ for analog data capture
- MATLAB for scripting of measurement and analysis algorithms and automation
- FPGA platform for pre silicon validation

Highlights

- Audio DSP algorithm being sophisticated, need to be validated for various cases.
- Created an automation environment where the HW modules and Matlab models coexist.
- With this environment in place, the setup time and execution time has been reduced.

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THANK YOU

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